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Term:

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ces Atisyments Claims

9. Document ID: US 6170001 B1

Full Title Citation Front Review Classification Date Refer≥nce Sagua

L1: Entry 9 of 33

File: USPT

Jan 2, 2001

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transfering format data from format register to memory wherein format data indicating the distribution of single or double precision data type in

the register bank

Fu		Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments.	Claims	KWIC	Draw, De
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Γ.]]	10.	Docume	ent ID	: US 6	061711 A					,		
I	ւ1։	Entr	y 10 o	f 33			File	: USPT			May 9,	200	00

US-PAT-NO: 6061711

DOCUMENT-IDENTIFIER: US 6061711 A

TITLE: Efficient context saving and restoring in a multi-tasking computing system

environment

Full	Title	Citation Front	Review Cl	assification	Date	Reference	SE REPORT	ettachments	Claims	KWIC	Draw, De
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П	11.	Document ID:	: US 605	8465 A			(

L1: Entry 11 of 33

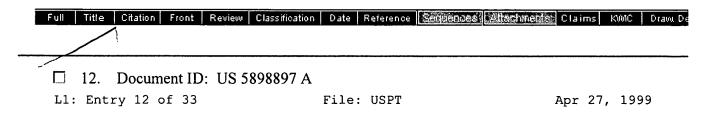
File: USPT

May 2, 2000

US-PAT-NO: 6058465

DOCUMENT-IDENTIFIER: US 6058465 A

TITLE: Single-instruction-multiple-data processing in a multimedia signal processor



US-PAT-NO: 5898897

DOCUMENT-IDENTIFIER: US 5898897 A

TITLE: Bit stream signal feature detection in a signal processing system

Full Title Citation Front Rev	ew Classification Date	Reference Sequences	Attachments Claims	KWMC Drawi De
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☐ 13. Document ID: US 5838984 A

L1: Entry 13 of 33

File: USPT

Nov 17, 1998

Oct 13, 1998

Apr 28, 1998

US-PAT-NO: 5838984

-DOCUMENT-IDENTIFIER: US 5838984 A

TITLE: Single-instruction-multiple-data processing using multiple banks of vector

registers

Full Title Citation Front Review Classification Date Reference Sequences Attachinents Claims KMC Draw De 14. Document ID: US 5821886 A

File: USPT

US-PAT-NO: 5821886

DOCUMENT-IDENTIFIER: US 5821886 A

L1: Entry 14 of 33

TITLE: Variable length code detection in a signal processing system

Full Title Citation Front Review Classification Date Reference Sequences Attachinents Claims RMC Draw De 15. Document ID: US 5778241 A

L1: Entry 15 of 33 File: USPT Jul 7, 1998

US-PAT-NO: 5778241

DOCUMENT-IDENTIFIER: US 5778241 A

TITLE: Space vector data path

L1: Entry 16 of 33

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWC Draw De 16. Document ID: US 5745721 A

File: USPT

US-PAT-NO: 5745721

DOCUMENT-IDENTIFIER: US 5745721 A

** See image for Certificate of Correction **

TITLE: Partitioned addressing apparatus for vector/scalar registers

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

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L1: Entry 17 of 33

File: USPT

Feb 10, 1998

US-PAT-NO: 5717881

DOCUMENT-IDENTIFIER: US 5717881 A

** See image for Certificate of Correction **

TITLE: Data processing system for processing one and two parcel instructions

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMIC Draw De

☐ 18. Document ID: US 5706490 A

L1: Entry 18 of 33

File: USPT

Jan 6, 1998

US-PAT-NO: 5706490

DOCUMENT-IDENTIFIER: US 5706490 A

** See image for Certificate of Correction **

TITLE: Method of processing conditional branch instructions in scalar/vector

processor

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 19. Document ID: US 5659706 A

L1: Entry 19 of 33

File: USPT

Aug 19, 1997

US-PAT-NO: 5659706

DOCUMENT-IDENTIFIER: US 5659706 A

** See image for Certificate of Correction **

TITLE: Vector/scalar processor with simultaneous processing and instruction cache

filling

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw De

☐ 20. Document ID: US 5640524 A

L1: Entry 20 of 33

File: USPT

Jun 17, 1997

US-PAT-NO: 5640524

DOCUMENT-IDENTIFIER: US 5640524 A

** See image for Certificate of Correction **

TITLE: Method and apparatus for chaining vector instructions

Full Title Citation Front Review Classification Date Reference Seguences Attachments Claims KWIC Draw De

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